Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S118	51812	"714".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:34
S121	312	711/125.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:35
S122	23	711/125.ccls. system adj4 chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:39
S123	8	711/125.ccls. system adj4 chip config$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:39
S124	214	711/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:36
S125	23	711/102.ccls. system adj4 chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:39
S126	0	"711".ccls. system adj4 chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:37
S127	1327	"711".clas. system adj4 chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:37

S12 8	1002	"711".clas. system adj4 chip config$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:37
S12 9	26	"711".clas. system adj4 chip config$7 watchdog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/16 10:44
S13 0	40	"711".clas. system adj4 chip config$7 (watchdog or watch adj dog)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:38
S13 1	0	711/125.ccls. system adj4 chip watchdog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:38
S13 2	4	711/102.ccls. system adj4 chip watchdog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:38
S13 3	0	711/125.ccls. system adj4 chip watch adj dog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:38
S13 4	0	711/102.ccls. system adj4 chip watch adj dog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/15 16:38
S13 5	41	"714".clas. system adj4 chip config$7 watchdog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/16 10:44
S13 6	0	("2005/0102573").URPN.	USPAT	AND	OFF	2005/12/16 10:45

S13 7	6	US-6323671-$.DID. OR US-6331463-$.DID. OR US-6459646-$.DID.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/16 11:24
S13 8	40	US-4879688-$.DID. OR US-5015885-$.DID. OR US-5042004-$.DID. OR US-5640344-$.DID. OR US-5739569-$.DID. OR US-5761120-$.DID. OR US-5768192-$.DID. OR US-5821581-$.DID. OR US-5901330-$.DID. OR US-5905674-$.DID. OR US-5943259-$.DID. OR US-5995744-$.DID. OR US-6028445-$.DID. OR US-6049222-$.DID. OR US-6094065-$.DID. OR US-6102963-$.DID. OR US-6105105-$.DID. OR US-6181597-$.DID. OR US-6272655-$.DID. OR US-6295230-$.DID.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/16 11:25
S14 3	20	US-4879688-$.DID. OR US-5015885-$.DID. OR US-5042004-$.DID. OR US-5640344-$.DID. OR US-5739569-$.DID. OR US-5761120-$.DID. OR US-5768192-$.DID. OR US-5821581-$.DID. OR US-5901330-$.DID. OR US-5905674-$.DID. OR US-5943259-$.DID. OR US-5995744-$.DID. OR US-6028445-$.DID. OR US-6049222-$.DID. OR US-6094065-$.DID. OR US-6102963-$.DID. OR US-6105105-$.DID. OR US-6181597-$.DID. OR US-6272655-$.DID. OR US-6295230-$.DID.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2005/12/16 11:26
S14 4	14	("5018096" | "5163147" | "5276839" | "5278976" | "5404494" | "5444664" | "5444861" | "5467286" | "5495593" | "5564032" | "5574926" | "5579479" | "5581723" | "5603056").PN.	US-PGPUB; USPAT; USOCR	AND	OFF	2005/12/16 11:47
S14 5	20	("5901330").URPN.	USPAT	AND	OFF	2005/12/16 11:47

S14 6	142951	pla	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:02
S14 7	7071	pla memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:02
S14 8	3040	pla memory microprocessor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:03
S14 9	3040	pla memory (microprocessor or uprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:03
S15 0	3067	pla memory (microprocessor or micro adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:04
S15 1	1967	pla memory config$7 (microprocessor or micro adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:04
S15 2	1515	pla memory configuration (microprocessor or micro adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:04
S15 3	765	pla memory configuration (microprocessor or micro adj processor) port	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:35

S154	43	"711".clas. pla memory configuration (microprocessor or micro adj processor) port	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:19
S155	31	"714".clas. pla memory configuration (microprocessor or micro adj processor) port	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:20
S157	61	"365".clas. pla memory configuration (microprocessor or micro adj processor) port	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 11:20
S158	16	pla memory configuration (microprocessor or micro adj processor) port encrypt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:49
S159	150	pla memory configuration (microprocessor or micro adj processor) port internet	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:53
S160	127	memory configuration (microprocessor or micro adj processor) port internet logic adj array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:54
S161	121	memory configuration (microprocessor or micro adj processor) port internet logic adj array interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:54
S165	3	"711".clas. memory configuration (microprocessor or micro adj processor) port internet logic adj array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:56

S166	7	"714".clas. memory configuration (microprocessor or micro adj processor) port internet logic adj array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:55
S167	3	"356".clas. memory configuration (microprocessor or micro adj processor) port internet logic adj array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:56
S168	5	"365".clas. memory configuration (microprocessor or micro adj processor) port internet logic adj array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/19 13:56
S170	3	"5247478".pn. or "5764096".pn. or "6122209".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2005/12/20 13:00
S171	1796	"circuit programming"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2005/12/20 13:00
S174	159	(in-circuit or in adj circuit) adj programming	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2005/12/20 13:02
S175	14	"711".clas. (in-circuit or in adj circuit) adj programming	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2005/12/20 13:03
S176	16	"714".clas. (in-circuit or in adj circuit) adj programming	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2005/12/20 13:06
S178	16	"365".clas. (in-circuit or in adj circuit) adj programming	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	AND	OFF	2005/12/20 13:19

S18 1	497	configuration adj load and (protect or protected or protection)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:07
S18 2	165	configuration adj load and (protect or protected or protection) memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:07
S18 3	7	"711".clas. configuration adj load and (protect or protected or protection) memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:14
S18 4	14	"714".clas. configuration adj load and (protect or protected or protection) memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:14
S18 5	5	"365".clas. configuration adj load and (protect or protected or protection) memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:52
S18 6	16	pla memory configuration (microprocessor or micro adj processor) port encrypt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:36
S18 7	0	"365".clas. configuration adj load and encrypt memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:52
S18 8	6	configuration adj load and encrypt memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 14:52

S189	304	protect$2 programming adj function	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 16:36
S190	5	"711".clas. protect$2 programming adj function	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 16:36
S191	1	"714".clas. protect$2 programming adj function	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 16:36
S192	37	"365".clas. protect$2 programming adj function	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/20 16:36
S194	2277	floating gate system adj4 chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	OFF	2005/12/21 14:25